What is claimed is:

- 1 1. A magnetic field sensor characterized by
- 2 comprising:
- a magnetic field element which outputs a signal in
- 4 accordance with an applied magnetic field strength in first
- 5 and second phases of a signal given from the outside of
- 6 said magnetic field element, wherein polarities of the
- 7 signal from said magnetic field element in said first phase
- 8 and said second phase are mutually opposite;
- 9 an amplifier which amplifies the signal from this
- 10 magnetic field element and outputs a voltage signal across
- 11 a pair of output terminals;
- 12 a condenser of which both ends are connected to the
- 13 pair of output terminals of said amplifier;
- 14 a switch which is inserted and makes a connection
- between one of said output terminals in the pair and one
- 16 terminal of said condenser, and which is closed in said
- 17 first phase of the signal and is opened in said second
- 18 phase of the signal; and
- 19 a comparator which inputs voltage across both ends of
- said switch and converts a result of the comparison of the
- 21 voltage across both ends of said switch with a
- 22 predetermined voltage into a binary signal so as to output.

- 2. A magnetic field sensor according to Claim 1,
 2 characterized by further comprising:
- a latch circuit which inputs said binary signal and outputs an either value of said binary signal, which is latched at the timing synchronized with a phase within said second phase of the signal.
- 3. A magnetic field sensor according to Claim 1, characterized in that said predetermined voltage of said comparator varies depending on the output signal of said latch circuit.
- 4. A magnetic field sensor according to Claim 1,
 2 characterized in that said magnetic field element is a Hall
 3 element.
- 5. A magnetic field sensor according to Claim 1
 characterized by further comprising a switch circuit for
 inputting a signal from said magnetic field element and
 outputting the signal to said amplifier, wherein
 - said switch circuit comprises first and second and memory elements; and

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in said first phase of the signal, the output voltage from said magnetic field element is stored in said first memory element and the voltage stored in said second memory

- 10 element is given to said amplifier and,
- in said second phase of the signal, the voltage stored
- in said first memory element is given to said amplifier and
- 13 the voltage from said magnetic field element is stored in
- 14 said second memory element.
- 1 6. A magnetic field sensor according to Claim 5,
- 2 characterized in that at least one memory element among
- 3 said memory elements is a capacitor.
- 7. A magnetic field sensor according to Claim 1
- characterized in that:
- 3 said magnetic field element outputs the signal from a
- 4 first terminal pair in said first phase of the signal and
- 5 the signal from a second terminal pair in said second phase
- of the signal in accordance with the applied magnetic field
- 7 strength; and a magnetic field sensor further comprises a
- 8 switch circuit for inputting the signal from said magnetic
- 9 field element and outputting the signal to said amplifier,
- 10 wherein
- 11 said switch circuit comprises:
- 12 first and second condensers;
- a first connection part which connects terminals of
- 14 said first terminal pair and both ends of said first
- 15 condenser, respectively;

- a second connection part which connects terminals of said second terminal pair and both ends of said second condenser, respectively;
- a first switch part which is inserted and makes a connection in said first connection part and which closes this first connection part in said first phase and opens this first connection part in said second phase;
- a second switch part which is inserted and makes a connection in said second connection part and which opens this second connection part in said first phase and closes this second connection part in said second phase;
- a third connection part which connects both ends of said first condenser to the input terminal of said amplifier as well as to one output terminal of said amplifier, respectively;

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- a fourth connection part which connects both ends of said second condenser to the input terminal of said amplifier as well as to said output terminal of said amplifier, respectively;
 - a third switch part which is inserted and makes a connection in said third connection part and which opens this third connection part in said first phase and closes this third connection part in said second phase; and
- a fourth switch part which is inserted and makes a connection in said fourth connection part and which closes

- this fourth connection part in said first phase and opens this fourth connection part in said second phase.
 - 1 8. A method for detecting magnetic field comprising 2 the steps of:
 - 3 (a) outputting a signal according to an applied 4 magnetic field strength through a magnetic field element in 5 a first signal period;

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- (b) outputting the signal according to the applied magnetic field strength through said magnetic field element in a second signal period, wherein polarities of the signals according to the applied magnetic field strength in said first signal period and said second signal period are mutually opposite;
 - (c) amplifying the signal from said magnetic field element in said first signal period for outputting a voltage signal across a pair of output terminals of an amplifier and inputting a signal of the pair of output terminals of said amplifier to both ends of a condenser;
 - (d) amplifying the signal from said magnetic field element in said second signal period for outputting a voltage signal across a pair of output terminals of said amplifier and inputting a signal of one output terminal in the pair to one end of said condenser, and outputting a signal across the other end of said condenser and the other

output terminal of said amplifier; 23

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- comparing the signal across the other end of said 24 condenser and the other output terminal of said amplifier 25 with a predetermined voltage; and
- converting the results of the comparison of the 27 (f) signal into a binary signal so as to output. 28
 - 9. A method for detecting magnetic field according 1 to Claim 8, characterized by further comprising a step of: 2
 - latching said binary signal at the timing (g) synchronized with a phase within said second signal period and outputting an either value of said binary signal.
 - A method for detecting magnetic field according 1 2 to Claim 8, characterized by further comprising a step of:
 - varying said predetermined voltage in accordance 3 with the output signal of said latching step. 4
 - A method for detecting magnetic field according 1 2 to Claim 8, characterized in that said magnetic field 3 element outputs a signal in accordance with a Hall effect.
 - A method for detecting magnetic field according 1 to Claim 8, characterized by further comprising a step of: 2
 - halting a power source supply to the magnetic 3 (i)

field element in every constant period.